UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,870,254 B1 Page 1 of 1

DATED : March 22, 2005 INVENTOR(S) : Estacio et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4,

Line 11, kindly delete "source and gate".

Line 13, kindly delete "that the solder bumps", and insert -- that the source and gate solder bumps --.

Signed and Sealed this

Thirteenth Day of September, 2005

JON W. DUDAS Director of the United States Patent and Trademark Office